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# Impedance-Based Harmonic Instability Assessment in Multiple Electric Trains and Traction Network Interaction System

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**Abstract**—This paper presents an impedance-based method to systematically investigate the interaction between multiple trains and traction network, focusing on evaluating the harmonic instability problem. Firstly, the interaction mechanism of multi-train and traction network is represented as a feedback interconnection of the two subsystems, i.e., an equivalent output impedance of the traction network and an equivalent input admittance of the multi-train. Then the harmonic instability is evaluated through a series of pole-zero diagrams drawn from the closed loop transfer matrix of the multi-train and network system (MTNS). The interaction system is unstable and the harmonic instability will happen if there are some high frequency poles of the closed loop system locating in the right half plane. This method is used for analyzing the harmonic instability phenomena, the characteristics, influential factors and potential mitigation schemes. The theoretical results are further validated by the simulations and experiments.

**Index Terms**—electrified railways, harmonic instability, impedance-based analysis, multi-train and network interaction.

## I. INTRODUCTION

### A. Motivations

BY the end of January 2017, the total mileage of the China high-speed railway (HSR) has exceeded 22 thousand kilometers, accounting for more than 60% in the world. It is vital to ensure the security and reliability of such a large scale transportation system. However, the current, voltage and phase-locked loop controllers of the high-speed trains (HSTs) may interact with each other and these controllers can further interact with the traction network, resulting in gradual amplification and instability in harmonic voltage and current of the traction network. This phenomenon has been named harmonic instability in [1-4].

These harmonic instability problems have been reported in China HSR lines:

1) The amplification phenomenon of the 47<sup>th</sup> to 55<sup>th</sup> harmonic current components occurred on the China HSR line in 2011. It led the control system of the HSTs to be unstable, the lightning arrestors of the HST to be burned, circuit breaker to

be tripped and many other malfunctions to be happened.

2) The high-frequency harmonic voltage was amplified gradually on the China HSR line during February 23<sup>rd</sup> to 24<sup>th</sup> in 2011, which made the control system of the HSTs unstable, voltage and current seriously distorted and four arrestors even exploded.

3) The harmonic current rose on the China HSR line in 2015 as well as caused the current transformer to be exploded and also the traction substation to be tripped.

Overall, the harmonic instability has restricted the development of China HSR. Additionally, the harmonic instability phenomenon in railway was first reported in Zürich, Switzerland in 1995 [1] and the same to high voltage direct current systems (HVDC) [2], wind generation [3] and solar power generation [4].

The standard EN50388-2012 [5] described and classified the overvoltage problems occurred in the AC or DC electrified railways as system instability and harmonic problems. The harmonic instability problems in the electrified railways are caused by the interaction between the four-quadrant converter (4QC) of HST and network. However, the specific cases or in-depth modeling analysis are not given in this standard.

Based on the previous works, the input admittance of multi-train is usually equivalent to  $n$  times of a single HST [6-8]. This method is effective for analyzing the low frequency oscillation problem caused by a large number of HSTs gaining access to the traction network at the same rail depot. However, the harmonic instability in electrified railway system usually occurs during the normal operation modes. The HSTs at different catenary positions are coupled with each other due to the network impedance. Therefore, the equivalent modeling method is not suitable for analyzing the harmonic instability in the MTNS.

With the rapid development of the HSR, the harmonic instability resulted from the interaction between multi-train and traction networks caught more attention. The MTNS is a multiple input multiple output (MIMO) system, there is coupling among multiple HSTs, as well as multiple HSTs and traction network, which makes it more difficult to determine the occurrence conditions and stability margin of harmonic instability. The harmonic instability in the MTNS has not been addressed yet.

### B. Literature Review

In order to identify the harmonic instability, the mainstream analysis methods can be classified into the following three categories: the nonlinear method [9], the state-space modeling

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method [10], and the impedance-based method [11]. The non-linear method has computational complexity and is difficult to study the large power electronics based power systems [12]. The state-space method needs to build state matrix of the interaction system and determine the oscillation mode based on eigenvalues and feature vectors of state matrix [10,13]. However, it is hard to establish detailed load model and dynamic network model with high switching frequency of the 4QC. In addition, adding or removing a source or a load, or changing the operation mode of a load need to re-establish the state matrix.

The impedance-based method has several advantages [14]: 1) A system impedance model and stability can be readily obtained and evaluated; 2) Changing the system structure and the source or load parameters only affect the impedance characteristics, hence the new system stability can be well assessed; 3) Impedance-based analysis can readily indicate possible solutions as well as damping design when a stability problem has been identified; 4) The impedance of the source or the load can be obtained through experiment or numerical simulation even when the system is a black box. Since the position of HST in the electrified railway is time-varying, the traction network structure and impedance will also be changed accordingly. Therefore, the impedance-based method is suitable for analyzing the stability of the interaction system between HST and network [5,6,8].

Currently, the impedance-based method is widely used for analyzing the stability of dynamical interconnected systems between the source (traction network) and the load (HST), such as wind and photovoltaic system [15], flexible HVDC system [16], railway system [6], DC microgrids [17], ship and aircraft power supply system [18] and so on. Based on the impedance analysis, there are following several criterions: impedance ratio criteria [19], positive-net-damping criteria [20], input-admittance criteria [21] and maximum peak criteria [22]. These methods are only suitable for a single input single output (SISO) system. For a MIMO system, the GNC [23] and the pole-zero of the closed loop transfer function [6] are the main methods for evaluating the system stability.

### C. Contributions

The aim of this paper is to investigate the harmonic instability characteristics of the MTNS in frequency domain based on pole-zero theory, which can predict and describe the harmonic instability phenomenon. The contributions of this paper are:

1) The impedance models of the MTNS, including: a) the impedance model of the HST in  $dq$  frame combining the voltage controller, the current controller and the phase-locked loop, see Section III; b) the impedance model of multiple trains which are coupled due to the traction network impedance, see Section IV.

2) In Section V, the methodology and the impact of variable parameters (e.g., the current controller, the voltage controller, the phase locked loop and traction network parameters) on the harmonic instability in the MTNS are fully investigated.

3) In Section VI, both simulation and experimental results have been conducted to validate the proposed method, which helps to improve the interactive performance between mul-

ti-train and traction network as well as mitigate the newly-found harmful electrical issues, see Section VII.

## II. MEASURED ANALYSIS

Fig. 1 shows the total harmonic distortion of the traction network voltage ( $U_{THD}$ ) under different HSR lines. The basic test conditions are as follows:

- *Test signals*: 1) the catenary voltage in Qionghai section post of a HSR line. 2) the catenary voltage in South Handan traction substation of a HSR line.
- *Test Methods*: the sampling interval is 2 seconds and the recording time is a continuous 24 hours.
- *Sampling frequency*: 50 kHz, that is, each sampling period is 1000 points.
- *$U_{THD}$  range*: the harmonic voltage of 2<sup>nd</sup>-100<sup>th</sup>.

In Fig. 1(a),  $U_{THD}$  is suddenly amplified and rapidly decreased in a short time. As mentioned in [24], Fig. 1(a) is a typical harmonic resonance phenomenon. However, in Fig. 1(b), it can be seen that the  $U_{THD}$  and the 61<sup>st</sup> harmonic component are gradually amplified in a certain time and the peak of the harmonic voltage appears at about 15:00. This phenomenon is different from the harmonic resonance in Fig. 1(a).

Fig. 2 depicts two typical voltage waveforms corresponding to Fig. 1(b). Fig. 2(a) is the voltage waveform whose  $U_{THD}$  is approximately 5%, corresponding to about 10:00 in Fig. 1(b). Fig. 2(b) is the voltage waveform whose  $U_{THD}$  is approximately 20%, corresponding to about 15:00. It can be seen that the harmonic components of the centenary voltage will be gradually amplified, which may make the onboard control system unstable, causes overvoltage problems with capacitive devices and arresters. As mentioned in [1], this phenomenon indicates the harmonic instability problem.

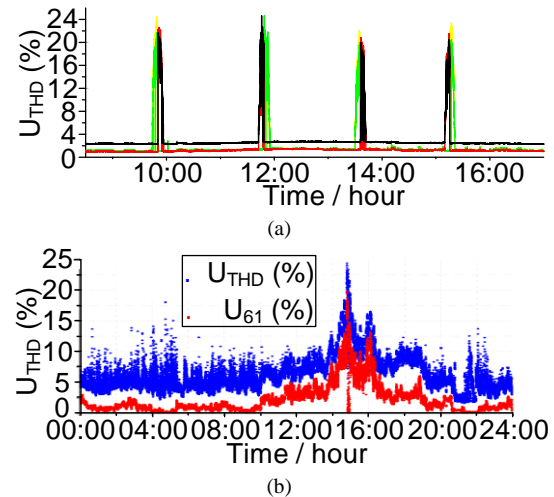
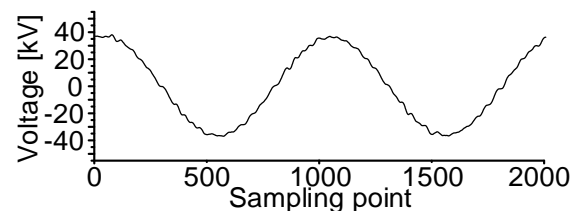


Fig. 1. Field test: (a)  $U_{THD}$  in Qionghai section post of a HSR line, China. (b)  $U_{THD}$  (blue) and the 61<sup>st</sup> harmonic component (red) in South Handan traction substation of a HSR line, China.



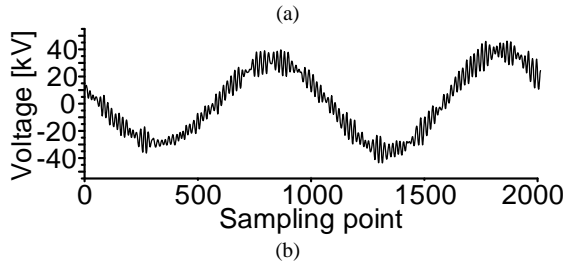


Fig. 2. Voltage waveform in South Handan traction substation of a HSR line: (a) The  $U_{THD}$  is approximately 5%; (b) The  $U_{THD}$  is approximately 20%.

### III. IMPEDANCE MODEL AND ANALYSIS OF A SINGLE HST AND NETWORK SYSTEM

The CRH5-type HST is one of the mainstream electrified trains in China HSR. Some controller modules, parameters and tuning principles have been introduced in [8]. The HST model can be divided into several parts according to its main circuit and controller structure. Meanwhile, for a nonlinear system, to permit stability analysis, linearization for small-signal perturbation quantities is needed. Then the analytical model of the impedance for the HST is performed along with the models of these parts together.

#### A. Main Circuit and Its DQ-Domain Admittance Model of the HST

The control block diagram of the HST is shown in Fig. 3. The control system includes the current controller (CC), voltage controller (VC) and phase-locked loop (PLL).  $L_{in}$  and  $R_{in}$  are the equivalent leakage inductance and resistance of the traction transformer, respectively.  $L_s$  and  $R_s$  are the equivalent inductance and resistance of the traction network, respectively.  $e_s$  is output voltage of the traction substation.

Notice that being different from the  $abc/\alpha\beta$  transformation of the three phase ac system, a second-order generalized integrator (SOGI) is usually used to transform the instantaneous value of measured single phase ac voltages and currents into a rotating reference framework ( $\alpha\beta$  quadrature signal). Therefore, there are two synchronous rotating frames, i.e., network  $dq$  frame and 4QC  $dq$  frame. The error angle  $\Delta\theta = \theta - \theta_0$  must be taken into account if there are some fluctuations around the steady-state point. In this paper, the superscript  $c$  is adopted for distinguishing the 4QC  $dq$  frame from the network  $dq$  frame.

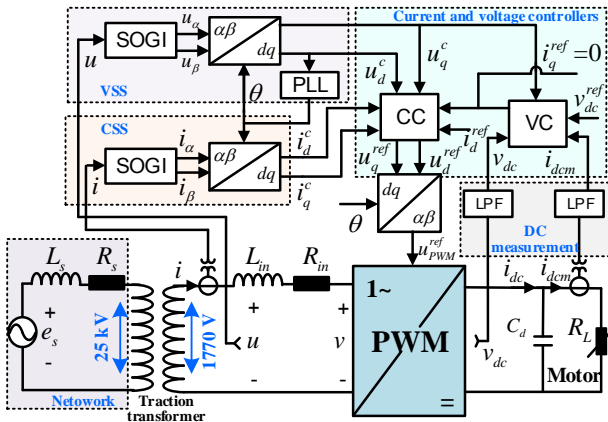


Fig. 3. A HST and network system including the control block diagram.

The analytical transfer functions derivation of the PLL, CC and VC are presented in Appendix A. The HST and network interaction system shown in Fig. 3 is split between the traction network and the HST subsystem. The input admittance of the HST can be given by

$$\Delta \mathbf{I}(s) = \left[ \mathbf{G}_{CC} \mathbf{G}_{VC} + \mathbf{Y}_{CC} \mathbf{G}_{uPLL} + \mathbf{G}_{iPLL} \right] \Delta \mathbf{U}(s)$$

$$\Rightarrow \Delta \mathbf{I}(s) = \underbrace{\begin{bmatrix} Y_{dd} & Y_{dq} \\ Y_{qd} & Y_{qq} \end{bmatrix}}_{\mathbf{Y}_{train}(s)} \Delta \mathbf{U}(s) \quad (1)$$

where  $\mathbf{G}_{uPLL}(s)$  and  $\mathbf{G}_{iPLL}(s)$  represent the voltage and current transfer functions of the PLL, respectively.  $\mathbf{Y}_{CC}(s)$  and  $\mathbf{G}_{CC}(s)$  are the input admittance and transfer function of the CC, respectively.  $\mathbf{G}_{VC}(s)$  represents the transfer function of the VC.

Fig. 4 depicts the impedance frequency responses of the HST where the simulation parameters in Appendix B are used. It can be seen that the negative damping in the low frequency domain of  $dd$  channel will be generated by the VC. In addition, the VC has empty impact on the impedance of  $qq$  channel. Similarly, the PLL only affects the impedance characteristics of the  $qq$  channel and  $qd$  channel since the synchronization feature of the PLL. For a rectifier, the positive damping characteristic in the low frequency domain of  $qq$  channel will be exhibited. In contrast, for an inverter, the negative damping characteristic in the low frequency domain will be obtained [23,25].

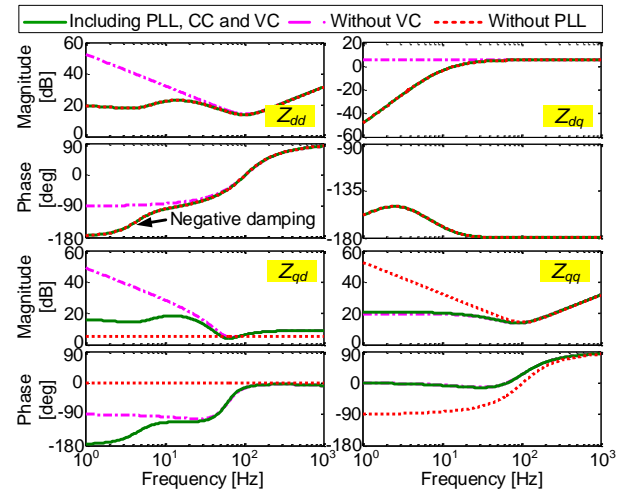


Fig. 4. Impedance frequency responses of the HST with different controllers. The green solid line denotes the impedance frequency responses of the HST including CC, VC and PLL. The pink chain dotted line and red dotted line denote the impedance frequency responses which only including CC, PLL and CC, VC, respectively.

#### B. Impedance Model of the Traction Network

The electrified railway traction system can be divided into ac and dc solutions. The dc traction system is mainly used in France, Japan (dc 11.5 kV) and Italy (dc 3 kV). The ac traction power supply system is widely used in the world, which has 15 kV / 16.67Hz [8], 25kV / 60Hz [26] and 25 kV / 50Hz modes. In addition, the 25 kV traction power system includes the 1×25 kV, (for the directly fed traction network, respect to the conventional railway) [27], and 2×25 kV, (for the AT-fed traction network, respect to the high-speed or overloaded railway),



which are mainly used in Korea [26], Italy [28], Europe [29], China [30-32], Iran [33] and Zimbabwe [34].

The AT-fed catenary system is generally adopted in China HSR. The mathematical model of the traction network mainly contains the equivalent circuit model, generalized symmetrical component model, and chain-circuit model [35]. The equivalent circuit model with resistance-inductance is widely used for tuning the control of inverters and studying the control performance [8,10]. For a single-phase ac system, the active and reactive power of the traction network can be considered as a  $dq$  system. From [6], the network impedance is transmitted as

$$\begin{bmatrix} \Delta u_d \\ \Delta u_q \end{bmatrix} = \underbrace{\begin{bmatrix} R_s + sL_s & -\omega_0 L_s \\ \omega_0 L_s & R_s + sL_s \end{bmatrix}}_{\mathbf{Z}_s(s)} \begin{bmatrix} \Delta i_d \\ \Delta i_q \end{bmatrix} \quad (2)$$

where  $\Delta$  denotes the small deviation of the respective variable from the equilibrium point. Compared with the input voltage  $\mathbf{U}(s)$  of the HST, the output voltage  $\mathbf{E}_s(s)$  of the traction substation has a higher voltage value due to the onboard traction transformer as shown in Fig. 3. In addition, the input admittance of the HST in Fig. 3 is only determined by the 4QC. Each HST has five same power units and those power units in a single train are all in parallel. Since each power unit is regarded as two same 4QC in parallel. Therefore, the minor-loop gain can be expressed as

$$\begin{aligned} \mathbf{L} &= [10 \cdot \mathbf{Y}_{train}(s)] \frac{\mathbf{Z}_s(s)}{k^2} = \mathbf{Y}_{train}(s) \left[ \frac{10 \cdot \mathbf{Z}_s(s)}{k^2} \right] \\ &= \mathbf{Y}_{train}(s) \mathbf{Z}_s^*(s) \end{aligned} \quad (3)$$

where  $k$  is the ratio of transformer.

### C. Stability Analysis of A Single HST and Network System

In this subsection, the impact of the network inductance ( $L_{01}^*$ ) and the proportional gain of CC ( $Kp_{CC}$ ) on the stability of a single HST and network system will be investigated by the generalized Nyquist criteria (GNC). The simulation parameters in Appendix B are used. Fig. 5 shows the impedance frequency responses of the HST and the network impedance. The red and blue solid lines denote the impedance of the network and HST, respectively. It can be seen that the diagonal elements of the network and HST have no interaction under the initial parameters. When increasing the  $L_{01}^*$  or  $Kp_{CC}$  (see the red and blue dotted line, respectively), the diagonal elements of the network and HST intersect with each other and the network impedance is inductive and the HST impedance is capacitive in  $dd$  channel. This phenomenon indicates that there is a risk of instability.

The minor-loop gain  $\mathbf{Y}_{train}(s)\mathbf{Z}_s^*(s)$  in (3) is taken into consideration and the GNC is applied to evaluate the stability. The stability conditions of the initial values (i.e., Case 1) can be predicted correctly as shown in Fig. 6(a). When the  $Kp_{CC}$  is 15 (i.e., Case 2) or  $L_{01}^*$  is 20 mH (i.e., Case 3), the characteristic locus ( $\lambda_2$ ) in Fig. 6 will encircle the critical point  $(-1, j0)$ , which means the system is unstable.

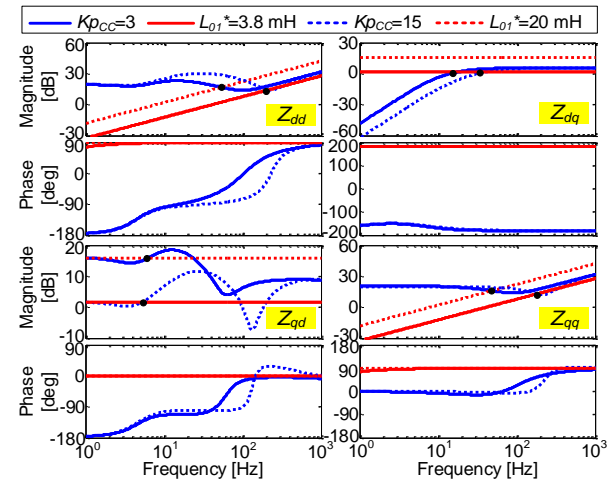


Fig. 5. Impedance frequency responses of the HST and traction network for different  $Kp_{CC}$  and  $L_{01}^*$ .

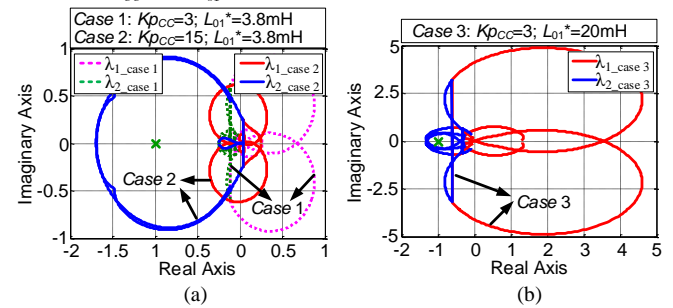


Fig. 6. Generalized Nyquist contours of (a) Case 1 and Case 2; (b) Case 3.

## IV. IMPEDANCE MODEL AND ANALYSIS OF THE MTNS

Different from the stability issues of a single HST system, the multi-train at different catenary positions are connected to the network impedance, as shown in Fig. 7. Therefore, an equivalent model of the HST with  $n$  times larger network cannot be applied. A impedance-based model is proposed to solve this problem.

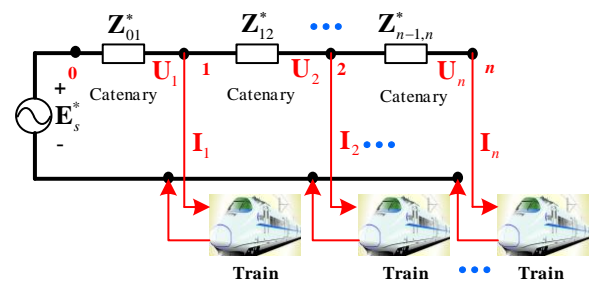


Fig. 7. A typical configuration of the MTNS.

### A. Impedance Model of the MTNS

The MTNS in Fig.7 is a multivariable system, in which the input and output vectors are the input voltages and currents of each HST. The  $i$ -th node demonstrates the position of the  $i$ -th HST connected to the network,  $\mathbf{Z}_{ij}^*(s)$  (with  $i, j=1,2,\dots,n; i \neq j$ ) represent the network impedance between node  $i$  and node  $j$ ,  $\mathbf{U}_i(s)$  and  $\mathbf{I}_i(s)$  (with  $i=1,2,\dots,n$ ) are the input voltage and the input current of the  $i$ -th HST, respectively. Applying the Kirchhoff law in Fig.7, one can obtain

$$\begin{cases} \mathbf{E}_s^*(s) - \mathbf{U}_1(s) = [\mathbf{I}_1(s) + \mathbf{I}_2(s) + \dots + \mathbf{I}_n(s)] \mathbf{Z}_{01}^*(s) \\ \mathbf{U}_1(s) - \mathbf{U}_2(s) = [\mathbf{I}_2(s) + \dots + \mathbf{I}_n(s)] \mathbf{Z}_{12}^*(s) \\ \dots \\ \mathbf{U}_{n-1}(s) - \mathbf{U}_n(s) = \mathbf{I}_n(s) \mathbf{Z}_{n-1,n}^*(s) \end{cases} \quad (4)$$

Equation (4) is added item by item, giving

$$\begin{cases} \mathbf{E}_s^* - \mathbf{U}_1 = (\mathbf{I}_1 + \dots + \mathbf{I}_n) \mathbf{Z}_{01}^* \\ \mathbf{E}_s^* - \mathbf{U}_2 = (\mathbf{I}_1 + \dots + \mathbf{I}_n) \mathbf{Z}_{01}^* + (\mathbf{I}_2 + \dots + \mathbf{I}_n) \mathbf{Z}_{12}^* \\ \dots \\ \mathbf{E}_s^* - \mathbf{U}_n = (\mathbf{I}_1 + \dots + \mathbf{I}_n) \mathbf{Z}_{01}^* + (\mathbf{I}_2 + \dots + \mathbf{I}_n) \mathbf{Z}_{12}^* + \dots + \mathbf{I}_n \mathbf{Z}_{n-1,n}^* \end{cases} \quad (5)$$

Referring to (2), (5) can be converted into the  $dq$  framework and the small-signal model can be then derived. The superscript  $m$  is used to distinguish multi-train from single train. Then, the  $2n \times 2n$  order network impedance matrix  $\mathbf{Z}_s^{*m}(s)$  in the  $dq$  framework is

$$\begin{bmatrix} \Delta u_{1d} \\ \Delta u_{1q} \\ \dots \\ \Delta u_{nd} \\ \Delta u_{nq} \end{bmatrix} = \begin{bmatrix} \mathbf{Z}_{(01)dd}^* & \mathbf{Z}_{(01)dq}^* & \dots & \mathbf{Z}_{(01)dd}^* & \mathbf{Z}_{(01)dq}^* \\ \mathbf{Z}_{(01)qd}^* & \mathbf{Z}_{(01)qq}^* & \dots & \mathbf{Z}_{(01)qd}^* & \mathbf{Z}_{(01)qq}^* \\ \dots & \dots & \dots & \dots & \dots \\ \mathbf{Z}_{(01)dd}^* & \mathbf{Z}_{(01)dq}^* & \dots & \sum_{i=1, \dots, n} \mathbf{Z}_{(i-1,i)dd}^* & \sum_{i=1, \dots, n} \mathbf{Z}_{(i-1,i)dq}^* \\ \mathbf{Z}_{(01)qd}^* & \mathbf{Z}_{(01)qq}^* & \dots & \sum_{i=1, \dots, n} \mathbf{Z}_{(i-1,i)qd}^* & \sum_{i=1, \dots, n} \mathbf{Z}_{(i-1,i)qq}^* \end{bmatrix} \begin{bmatrix} \Delta i_{1d} \\ \Delta i_{1q} \\ \dots \\ \Delta i_{nd} \\ \Delta i_{nq} \end{bmatrix} \quad (6)$$

$$\Rightarrow \Delta \mathbf{U}^m(s) = \mathbf{Z}_s^{*m}(s) \Delta \mathbf{I}^m(s)$$

where  $\Delta \mathbf{U}^m(s)$  is the output voltage vector containing the input voltage  $\Delta \mathbf{U}_i(s)$ ,  $\Delta \mathbf{I}^m(s)$  is the input current vector representing the input current  $\Delta \mathbf{I}_i(s)$ .

Similarly, for each HST in Fig.7, the admittance matrix of the HSTs can be written as (7)

$$\begin{bmatrix} \mathbf{I}_1(s) \\ \mathbf{I}_2(s) \\ \dots \\ \mathbf{I}_n(s) \end{bmatrix} = \begin{bmatrix} \mathbf{Y}_{11}(s) & \mathbf{Y}_{12}(s) & \dots & \mathbf{Y}_{1n}(s) \\ \mathbf{Y}_{21}(s) & \mathbf{Y}_{22}(s) & \dots & \mathbf{Y}_{2n}(s) \\ \dots & \dots & \dots & \dots \\ \mathbf{Y}_{n1}(s) & \mathbf{Y}_{n2}(s) & \dots & \mathbf{Y}_{nn}(s) \end{bmatrix} \begin{bmatrix} \mathbf{U}_1(s) \\ \mathbf{U}_2(s) \\ \dots \\ \mathbf{U}_n(s) \end{bmatrix} \quad (7)$$

The diagonal element  $\mathbf{Y}_{ii}(s)$  is equal to the self-admittance of the  $i$ -th HST, representing the influence of the input voltage  $\mathbf{U}_i(s)$  on the input current  $\mathbf{I}_i(s)$ .  $\mathbf{Y}_{ij}(s)$  is the mutual-admittance between the  $i$ -th and  $j$ -th HSTs, that is, the influence of the other HST's input voltages  $\mathbf{U}_j(s)$  on the studied input current  $\mathbf{I}_i(s)$ . Different from the grid-connected inverters shown in [36], the HST is regarded as a load absorbing the active power from the traction network. If the regenerative braking mode is not considered, there are no coupling between every branch current  $\mathbf{I}_i(s)$ , which means the mutual-admittance is  $\mathbf{Y}_{ij}(s) = 0$ . Referring to (1), the  $2n \times 2n$  order HSTs admittance matrix  $\mathbf{Y}_{train}^m(s)$  in the  $dq$  framework is then rearranged as

$$\begin{bmatrix} \Delta i_{1d} \\ \Delta i_{1q} \\ \dots \\ \Delta i_{nd} \\ \Delta i_{nq} \end{bmatrix} = \begin{bmatrix} \mathbf{Y}_{(1,1)dd} & \mathbf{Y}_{(1,1)dq} & \dots & 0 & 0 \\ \mathbf{Y}_{(1,1)qd} & \mathbf{Y}_{(1,1)qq} & \dots & 0 & 0 \\ \dots & \dots & \dots & \dots & \dots \\ 0 & 0 & \dots & \mathbf{Y}_{(n,n)dd} & \mathbf{Y}_{(n,n)dq} \\ 0 & 0 & \dots & \mathbf{Y}_{(n,n)qd} & \mathbf{Y}_{(n,n)qq} \end{bmatrix} \begin{bmatrix} \Delta u_{1d} \\ \Delta u_{1q} \\ \dots \\ \Delta u_{nd} \\ \Delta u_{nq} \end{bmatrix} \quad (8)$$

$$\Rightarrow \Delta \mathbf{I}^m(s) = \mathbf{Y}_{train}^m(s) \Delta \mathbf{U}^m(s)$$

### B. Impedance-Based Stability Assessment Method

The impedance or admittance matrix of the traction network and multi-train in the  $dq$ -domain are both  $2n \times 2n$  matrices, as expressed in (6) and (8). The output voltage and current used in the network impedance matrix and multi-train admittance matrix are the same, their small signal variations can depict a multivariable feedback system, as shown in Fig. 8(a), where the input of closed loop system is the network voltage  $\Delta \mathbf{E}_s^*(s)$ , the output is the network current  $\Delta \mathbf{I}^m(s)$ . It can be seen as a block diagram generated by the Thevenin representation of Fig. 8(b).

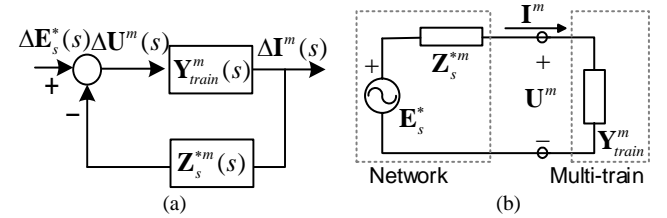


Fig. 8. Multi-train modeling (a) block diagram of multivariable feedback system; (b) Thevenin representation of the MTNS.

Then, the closed loop transfer matrix of the MTNS can be expressed as

$$\mathbf{H}(s) = \frac{\mathbf{Y}_{train}^m(s)}{\mathbf{I}_{2n} + \mathbf{Y}_{train}^m(s) \cdot \mathbf{Z}_s^{*m}(s)} \quad (9)$$

where  $\mathbf{I}_{2n}$  is a  $2n \times 2n$  unity diagonal matrix. As the number of HSTs increase, the characteristic locus of the minor-loop gain  $\mathbf{Y}_{train}^m(s) \mathbf{Z}_s^{*m}(s)$  will increase. Correspondingly, Pole-zero analysis is a powerful tool to investigate the linear system stability. To keep a multivariable feedback system stable, all the poles of the closed loop transfer matrix must lie in the left-half of the complex  $s$ -plane [37]. Therefore, the MTNS is stable if and only if the closed loop transfer matrix in (9) have no poles located in the right half plane (RHP). Notice that the network feedback transfer matrix  $\mathbf{Z}_s^{*m}(s)$  has no poles and is naturally stable. Additionally, the transfer matrix of multi-train  $\mathbf{Y}_{train}^m(s)$  will be stable in a suitable parameter range. Therefore, the possible RHP poles of the closed loop transfer matrix  $\mathbf{H}(s)$  are the main reasons for the instability phenomenon.

### V. INFLUENCE FACTORS OF THE HARMONIC INSTABILITY

To investigate the harmonic instability of the MTNS, the trajectory of poles of the closed loop transfer matrix  $\mathbf{H}(s)$  in (9) are computed to evaluate the harmonic stability. The network and controller parameters of the simulation and experimental system are listed in Appendix B.

### A. Impact of the Number of HSTs

There are usually no more than three running HSTs in a same power supply phase during the normal operation modes. Fig. 9 depicts the poles traces of the MTNS as the HST number increases, where the HSTs are coupled due to the network impedance. There are some conjugate pairs dominating the high frequency (higher than several hundred rad/s) stability characteristic, and the other conjugate pairs are dominating the low frequency (below 50 rad/s) stability characteristic. In Fig. 9, an increasing number of HSTs pose more complex closed loop transfer matrix and more poles. The poles traces of an individual HST and two HSTs are located in the left-half plane and indicate that the individual HST and two HSTs are able to operate stably. Once the number of HSTs exceed three, some poles are located in the right-half plane, which means the system will be unstable.

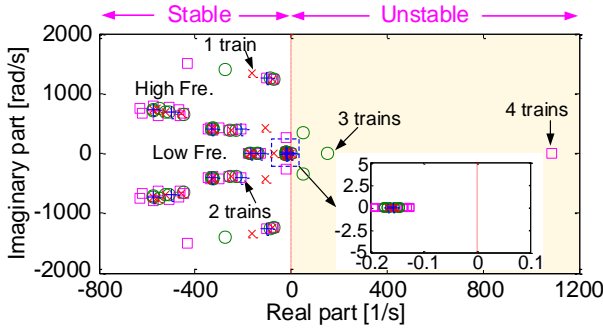


Fig. 9. Trajectory of poles as the HST numbers are increased from 1 to 4. The red spot 'x' denotes a single HST, the blue spot '+', green spot 'o' and purple spot '□' denote the two HSTs, three HSTs and four HSTs, respectively.

Note that the valid frequency width of the multi-train admittance matrix is up to the Nyquist frequency  $f_s/2$  as the PWM modulation uses the average model. In addition, for a single-phase ac system, the  $dq$  decoupling method is commonly used to obtain the steady-state operating points. The trajectory of poles in Fig. 9 are all located in an available frequency range, illustrating that the impedance-based is efficient.

### B. Impact of Controller Parameters

The trajectory of the poles when changing proportional gain of the CC, PLL and VC are shown in Fig. 10. Two running HSTs are considered and the initial parameters listed in Appendix B are used. In Fig. 10(a), the proportional gain of the current controller ( $K_{p_{CC}}$ ) ranges from 3 to 7 by a step of 1, where a high frequency complex conjugate pole moves toward the RHP as  $K_{p_{CC}}$  increases. The system will be unstable once the  $K_{p_{CC}}$  is more than 4.05. Similarly, in Fig. 10(b) and (c), the proportional gain of the PLL ( $K_{p_{PLL}}$ ) and VC ( $K_{p_{VC}}$ ) ranges from 0.5 to 4.5 by a step of 1, and 0.4 to 2 by a step of 0.4, respectively. The high frequency complex conjugate poles move toward the RHP as  $K_{p_{PLL}}$  decreases or  $K_{p_{VC}}$  increases. When  $K_{p_{PLL}}$  is smaller than 0.75 or  $K_{p_{VC}}$  is larger than 1.25, the system will have some high frequency poles located in the RHP, indicating the occurrence of the harmonic instability. In addition, it can be seen that the low frequency poles are insen-

sitive to the controller parameter. Thus, the high frequency complex conjugate poles dominate the harmonic instability phenomenon in the condition.

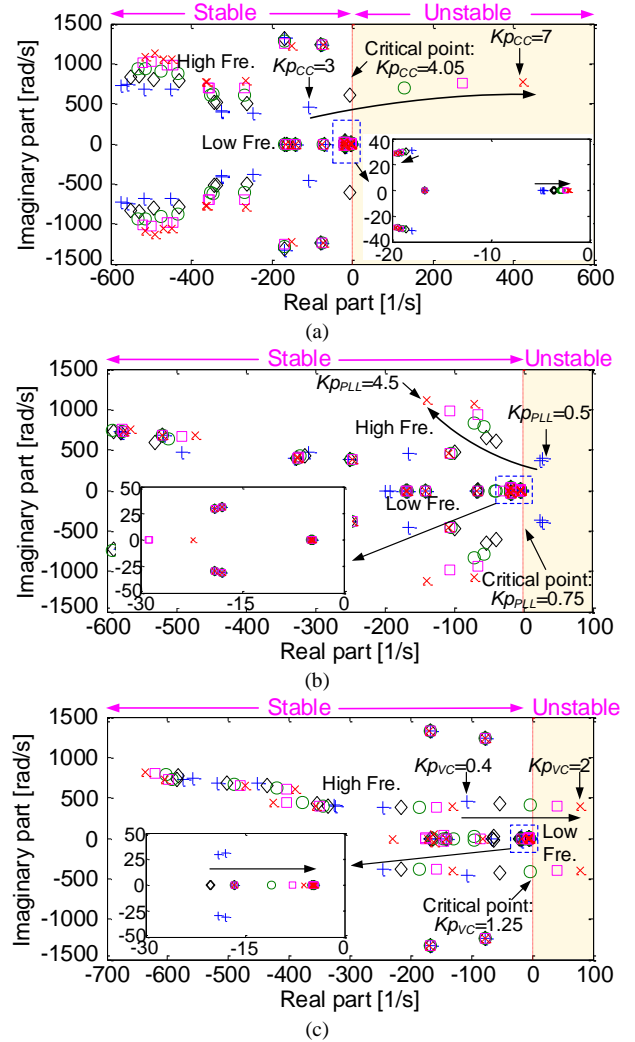


Fig. 10. Trajectory of poles when increasing (a)  $K_{p_{CC}}$  ( $3 < K_{p_{CC}} < 7$ , step: 1), (b)  $K_{p_{PLL}}$  ( $0.5 < K_{p_{PLL}} < 4.5$ , step: 1) and (c)  $K_{p_{VC}}$  ( $0.4 < K_{p_{VC}} < 2$ , step: 0.4). The blue spot '+' denotes the initial value, then the black spot '◇', the green spot 'o', purple spot '□' and red spot 'x' denote the second, third, fourth and fifth calculation, respectively.

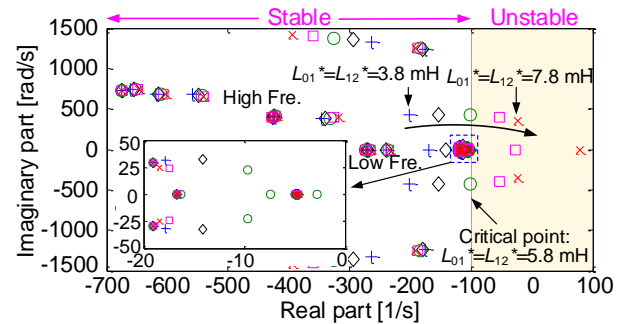


Fig. 11. Trajectory of poles when increasing the network inductances  $L_{01}$ ,  $L_{12}$  ( $3.8 \text{ mH} < L_{01}, L_{12} < 7.8 \text{ mH}$ , step with 1 mH).

### C. Impact of the Network Inductance

The network impedance consists of the resistive and inductive components. To explain the effects of the network inductance ( $L_{01}^*$ ,  $L_{12}^*$ ) on harmonic instability, the trajectory of poles with different network inductance is given in Fig. 11. When  $L_{01}^*$  and  $L_{12}^*$  are synchronization increased from 3.8 mH to 7.8 mH, a high frequency complex conjugate pole and a real root move toward the RHP as  $L_{01}^*$ ,  $L_{12}^*$  increases meanwhile the threshold of the instability is 5.8 mH.

## VI. SIMULATIONS AND EXPERIMENTS

### A. Simulation Results

In order to validate the proposed theoretical analysis, time-domain simulations of the MTNS are shown in Fig. 7 and the control system in Fig. 3 is performed based on Matlab/Simulink software. The parameters given in TABLE BI of Appendix B are fully considered.

Fig. 12 illustrates the simulation results when a single HST system is in the unstable condition. Fig. 12(a) denotes that  $Kp_{CC}$  increases from 3 to 15 at 0.25s and Fig. 12(b) depicts that  $L_{01}^*$  increases from 3.8 mH to 20 mH at 0.1s. The harmonic components of the voltage are amplified and the system becomes unstable, which agrees with the GNC analysis in Fig. 6.

The simulation results of increasing the number of the connected HSTs are shown in Fig. 13. The HST #1 and HST #2 are connected stably. Once the HST #3 is activated and connected into catenary at 0.2 s, the harmonic components of the network voltage are observed. When the HST #4 is activated and connected in the catenary, the harmonic components are amplified and the whole system becomes unstable. The result agrees well with the poles in Fig. 9.

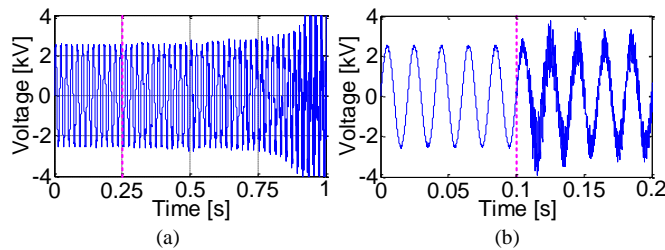


Fig. 12. Unstable case of a single HST network system: (a)  $Kp_{CC}$  increases from 3 to 15 at 0.25s; (b)  $L_{01}^*$  increasing from 3.8 mH to 20 mH at 0.1s.

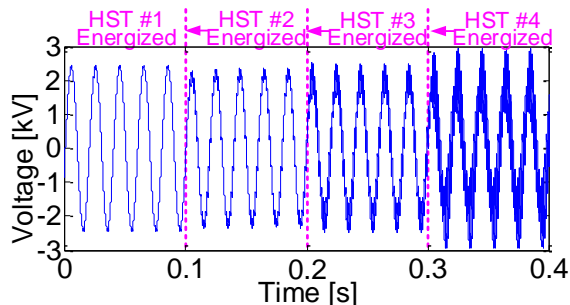


Fig. 13. Simulation results as the number of HSTs are increased.

Fig. 14(a), (b) and (c) depict the simulation results when the

proportional gain  $Kp_{CC}$ ,  $Kp_{PLL}$  and  $Kp_{VC}$  in the CC, PLL and VC are in the unstable region. The interaction system will turn to become unstable after changing the corresponding parameters of these controllers. In Fig. 14(a), (b) and (c), the harmonic components of the network voltage are gradually amplified and the system becomes unstable. The results demonstrate the theoretical result in Fig. 10. Meanwhile, the harmonic instability in the MTNS is mainly caused by the instability behavior of the controller.

As shown in Fig. 14(d), the harmonic components of the network voltage are amplified after synchronization increases the network inductance  $L_{01}^*$  and  $L_{12}^*$  from 3.8 mH to 6.8 mH at 0.1s. The results agree well with the theoretical results shown in Fig. 11.

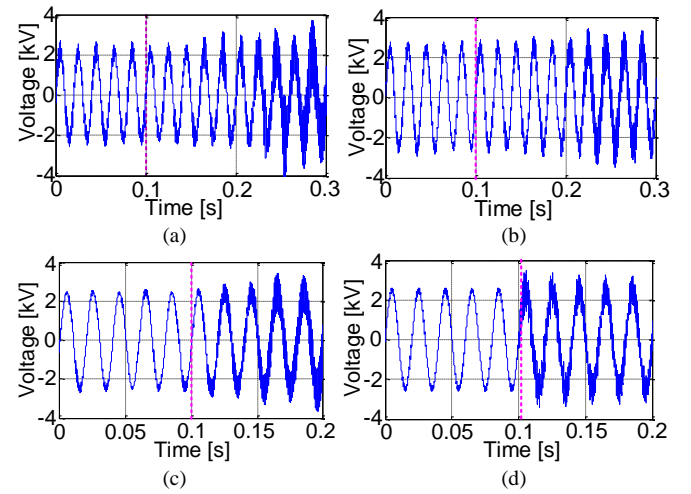


Fig. 14. Simulation results when a step change of  $Kp_{CC}$ ,  $Kp_{PLL}$ ,  $Kp_{VC}$  and network impedance: (a)  $Kp_{CC}$  increases from 3 to 5 at 0.1s; (b)  $Kp_{PLL}$  decreases from 6 to 0.5 at 0.1s; (c)  $Kp_{VC}$  increases from 0.4 to 1.4 at 0.1s; (d) The  $L_{01}^*$  and  $L_{12}^*$  synchronization increase from 3.8 mH to 6.8 mH at 0.1s.

### B. Experimental Results

To further validate the proposed impedance calculation method, a scaled down experimental platform with two HSTs are carried out. The hardware picture and circuit configuration of the experimental platform is shown in Fig. 15 and its parameters are listed in TABLE BI. In this experimental system, the rated power of each HST is 88.2 W with 21 V ac input voltage and 42 V dc output voltage. Meanwhile, its input voltage is isolated through a single-phase step-down transformer (240 V/21 V). Additionally, the IGBTs (1MBH60D-100) are chosen for the 4QC and the TX-DA962D4 is used as drive circuit. The switching frequency is set to 1250 Hz. SC is the signal conditioner. The control system is implemented into the DSP (TMS320F28335). The waveforms are recorded by the oscilloscope (Agilent DSO-X3034A). The control parameters of the system can be altered in the host computer through bluetooth module.



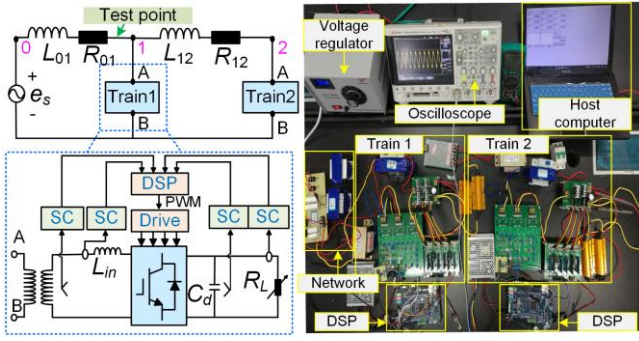


Fig. 15. Experimental setup of the MTNS.

Based on the impedance-based stability assessment method in Section IV, the trajectory of the poles with two HSTs when increasing the values of the  $Kp_{cc}$  and network inductances are shown in Fig. 16. As a result, with the increase of  $Kp_{cc}$  from 6.5 to 10.5 by a step of 1, a complex conjugate pole moves toward the right half-plane (unstable region). Similarly, with the synchronization increase of  $L_{01}^*$  and  $L_{12}^*$  from 3.2 mH to 7.2 mH by a step of 1 mH, a complex conjugate pole and a real root move toward the right half-plane. When  $Kp_{cc}$  is larger than 7.75 or  $L_{01}^*$  and  $L_{12}^*$  are larger than 5 mH, the system will have a high frequency complex conjugate pole located in the RHP, indicating the occurrence of the harmonic instability.

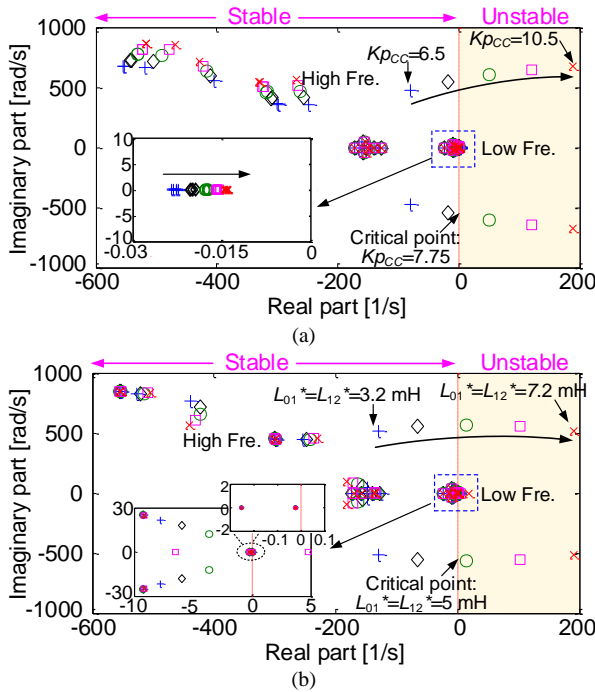


Fig. 16. Trajectory of poles for an increase of (a) the current controller proportional gain  $Kp_{cc}$  and (b) the network inductances  $L_{01}^*$ ,  $L_{12}^*$ .

The experimental results when changing  $Kp_{cc}$  of paralleled two HSTs are shown in Fig. 17. Two sets of  $Kp_{cc}$ , i.e.,  $Kp_{cc} = 6.5$  and  $Kp_{cc} = 9$ , are tested. It is clear that the voltage and current contain a large number of harmonic components and the experimental system becomes unstable when

$Kp_{cc}$  is increased to 9, which is closely correlated with the theoretical analysis in Fig. 16(a).

Fig. 18 depicts the experimental results of different network inductances with two paralleled HSTs. Three sets of  $L_{01}^*$ ,  $L_{12}^*$ , i.e.,  $L_{01}^* = L_{12}^* = 7.2 \text{ mH} / 3.2 \text{ mH} / 7.2 \text{ mH}$ , are tested. The harmonic components of the voltage and current will be gradually amplified when  $L_{01}^*$ ,  $L_{12}^*$  are increased to 7.2 mH. Therefore, a reduced network inductance is needed to inhibit harmonic stability. It agrees with the theoretical results shown in Fig. 16(b).

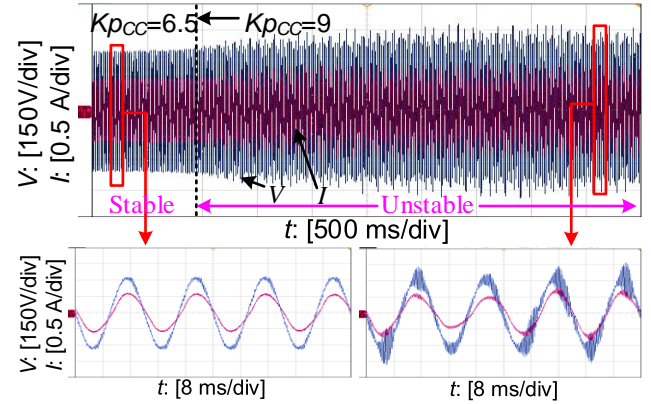


Fig. 17. Measured network voltage and current when a step change of  $Kp_{cc}$  from 6.5 to 9.

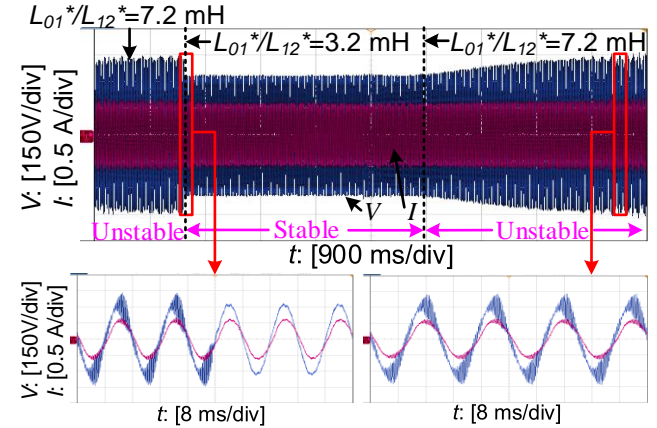


Fig. 18. Measured network voltage and current with a step change of network inductances  $L_{01}^*$ ,  $L_{12}^*$ .

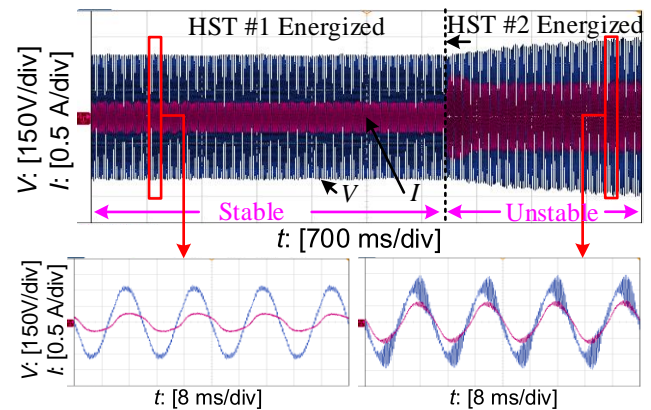


Fig. 19. Measured network voltage and current as the number of HSTs are increased.

Fig. 19 depicts the experimental results with different numbers of HSTs. The  $Kp_{cc}$  of two HSTs are both set to 9 for obtaining an unstable condition. It can be seen that a single HST is stable. Once the HST #2 is activated and connected to the network, the harmonic is amplified and the whole system becomes unstable. The results indicate the number of HSTs have a significant effect on stability.

### C. Summary

The harmonic instability is closely related to harmonic resonance phenomenon that the resonance is easily arisen when the interaction system is unstable. In fact, different influential factors are equivalent to regulate the impedance transfer matrix of the interaction system, which can further change the poles distribution of the closed loop transfer matrix. Therefore, it may generate an unstable region by continuously changing the network parameters and controller parameters of the HSTs. The harmonic instability region with different system parameters are not the same, but the simulations and experiments have the same trend with the calculations, which verify the analysis and the proposed method. The unstable regions in the calculations, simulations and experiments are listed in TABLE I. For a specific railway line analysis, the system stability can be evaluated with specific parameters by using the proposed impedance-based model.

TABLE I  
HARMONIC INSTABILITY FACTORS

Subject	Factor	Instability region
Simulation	Number of the HSTs	$> 2$
	CC parameter $Kp_{cc}$	$\geq 4.05$
	PLL parameter $Kp_{PLL}$	$\leq 0.75$
	VC parameter $Kp_{VC}$	$\geq 1.25$
	Network inductance $L_{01}, L_{12}$	$\geq 5.8$ mH
Experiment	Number of the HSTs	$> 2$
	CC parameter $Kp_{cc}$	$\geq 7.75$
	Network inductance $L_{01}, L_{12}$	$\geq 5$ mH

## VII. DISCUSSION AND SUGGESTIONS

### A. Differences Between Harmonic Instability and Harmonic Resonance

The harmonic resonance risk is inherent in any industrial power systems. However, not all resonances can cause serious harmonic instability problem. The resonance may easily generate the harmonic instability problem with active power-electronics devices because of the possible unstable interactions between the traction network and the HST. The standard EN50388-2 provides a method to suppress the harmonic instability problems in the train and network interaction system [5]: An electrified traction vehicle shall be passive above a certain frequency  $f_L$ , and the lowest resonance in the traction network shall be greater than  $f_L$ . This is a conservative method.

The harmonic voltage spectrum in the stable and unstable

condition are plotted in Fig. 20 demonstrates that not all resonance will excite the harmonic instability only if the high frequency poles are located in the RHP. When the interaction system has no high frequency poles in the RHP, as shown in Fig. 20(a), the interaction system is stable and there are harmonic resonances around the 29 pu, 49 pu and 51 pu. If the interaction system has high frequency poles in the RHP, the harmonic voltage around 29 pu will be gradually amplified, as shown in Fig. 20(b), but the harmonic voltage magnitude around 49 pu and 51 pu are still there.

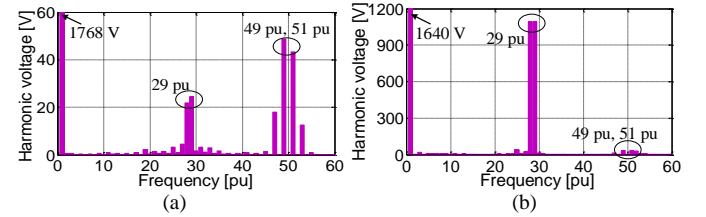


Fig. 20. Harmonics voltage spectrum with different pole distributions: (a) no high frequency poles located in the RHP (stable); (b) high frequency poles located in the RHP (unstable).

### B. Mitigation Suggestions for the Harmonic Instability

The impedance-based model of the interaction system plays a dominant role when studying the harmonic instability. Based on the obtained results and in order to avoid and attenuate the harmonic instability problems, the following methods can be adopted:

1) *Influential parameters adjustment.* The harmonic instability in the interaction system of different HST and network parameters, which has an unstable region as shown in TABLE I. If the parameters fall into an unstable region, the system will be unstable, then the harmonic voltage or current will be gradually amplified. In addition, reducing or increasing those parameters can help them escape from the unstable region. Fig. 21 depicts the experimental result of decreasing the proportional gain of the CC. The harmonic instability phenomenon got a good inhibition, which verified the theoretical result in Fig. 16(a). Therefore, it is necessary to select a suitable bandwidth of the control loops.

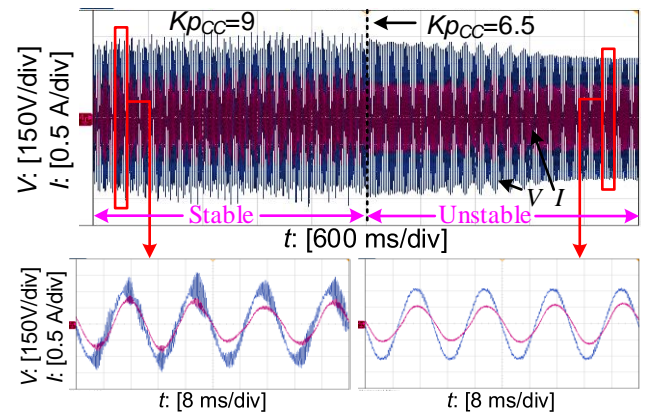


Fig. 21. Measured network voltage and current with a step change of current controller proportional gain  $Kp_{cc}$  from 9 to 6.5.

2) *Additional mitigation devices installation.* By using a damping resistor in series with the filter capacitor C and LCL filter to damp the unstable poles and decrease the resonance

peak in the Bode diagram is a possibility [38]. Fig. 22 depicts the experimental results of putting in first-order high-pass filter, which has a better performance in mitigating harmonic instability. However, the additional impedance will generate the power losses and decrease the efficiency of the power system. Hence, the method of additional passive damping is applicable for small or middle photovoltaic and wind power system.

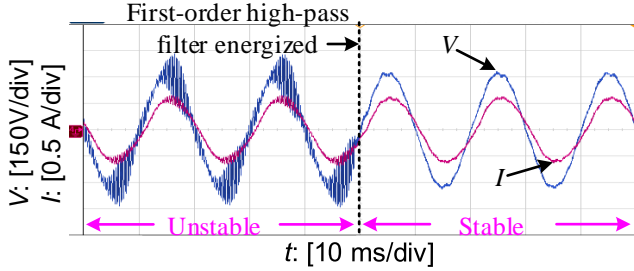


Fig. 22. Measured network voltage and current when a first-order high-pass filter is energized.

3) *Traction network enhancement.* the HST and network interaction system is prone to the instability problem under a weak network condition [14]. The experimental results in Fig. 18 verify this conclusion. Therefore, the economy, reliability and stability should be comprehensively considered. During the line design stage or transformation stage, the traction network impedance (especially for the network inductance) can be reduced for a more stable system.

## VIII. CONCLUSIONS

In this paper, in order to investigate the formation and influential factors of the harmonic instability phenomena in the electrified railways, an impedance-based method and the interaction between multi-train and traction network is presented. Firstly, the impedance model of the HST includes a phase-locked loop, current controller, and voltage controller. Then, an overall impedance model containing the multi-train and traction network is proposed. The harmonic instability is well assessed by analyzing the trajectory of poles in the closed loop impedance transfer matrix. The analytical results present the controller's parameters of HST, the number of HSTs and the traction network inductance all have an essential effect on the harmonic instability in the MTNS. The proposed method of harmonic instability and available influential factors have been validated through simulations and experiments. If possible, mitigation measures, including influential parameters adjustment, additional mitigation devices installation and traction network enhancement can be options to solve the problem.

## APPENDIX A

### DQ-DOMAIN ADMITTANCE MODELING OF THE HST

#### A. Phase-Locked Loop Model

The voltage synchronization control block diagram is shown in Fig. A1. Steinar Danielsen in [8] introduced the SOGI transfer function in (10).

$$\mathbf{H}_{eSOGI} = \begin{bmatrix} H_{eSOGI} & 0 \\ 0 & H_{eSOGI} \end{bmatrix}, H_{eSOGI} = \frac{1}{\frac{s}{K_{eSOGI}} \left( \frac{1}{w_0} + \frac{T_0}{8} \right) + 1} \quad (10)$$

where  $K_{eSOGI}$  represents the proportional gain of the SOGI in the voltage synchronization system,  $w_0$  is the fundamental angular frequency and  $T_0$  is the fundamental sampling time.

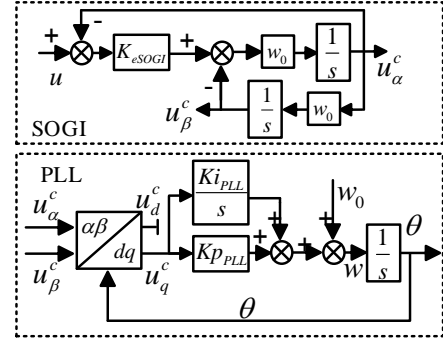


Fig. A1. Control block diagram of voltage synchronization system.

From Fig. A1, introducing the phase-locked error  $\Delta\theta = \theta - \theta_0$  and  $w_0 = s\theta_0$ , one can obtain

$$s\theta = w_0 + \underbrace{\left( Kp_{PLL} + \frac{Ki_{PLL}}{s} \right)}_{F_{PLL}} u_q^c \Rightarrow s\Delta\theta = F_{PLL} u_q^c \quad (11)$$

where the error angle  $\Delta\theta$  is sufficiently small and there exists the following relationship

$$\begin{aligned} \mathbf{U}^c &= \mathbf{H}_{eSOGI} e^{-j\Delta\theta} \mathbf{U} \approx \mathbf{H}_{eSOGI} (1 - j\Delta\theta) (\Delta\mathbf{U} + \mathbf{U}_0) \\ \Rightarrow \mathbf{U}^c &\approx \mathbf{H}_{eSOGI} (\Delta\mathbf{U} - j\mathbf{U}_0 \Delta\theta) \end{aligned} \quad (12)$$

The  $u_{q0}$  is equal to zero in steady-state, so  $\mathbf{U}_0 = u_{d0}$  and  $u_q^c \approx H_{eSOGI} (\Delta u_q - u_{d0} \Delta\theta)$  can be obtained. Then together with (11),  $\Delta\theta$  can be written as

$$\Delta\theta = \frac{F_{PLL} H_{eSOGI}}{s + u_{d0} F_{PLL} H_{eSOGI}} \Delta u_q \quad (13)$$

Inserting (13) into (12), the voltage transfer matrix of the PLL is

$$\begin{bmatrix} \Delta u_d^c \\ \Delta u_q^c \end{bmatrix} = \underbrace{\begin{bmatrix} H_{eSOGI} & 0 \\ 0 & H_{eSOGI} - H_{eSOGI} u_{d0} G_{PLL} \end{bmatrix}}_{G_{ePLL}(s)} \begin{bmatrix} \Delta u_d \\ \Delta u_q \end{bmatrix} \quad (14)$$

Correspondingly, the current synchronization system has the same structure with the voltage synchronization system apart from the PLL. Referring to (12) and ignoring the effect of the SOGI on network current, the current of the different frames can be linearized as

$$\mathbf{I}^c = \mathbf{H}_{iSOGI} e^{-j\Delta\theta} \mathbf{I} \Rightarrow \mathbf{I}^c \approx \Delta\mathbf{I} - \mathbf{H}_{iSOGI} j\mathbf{I}_0 \Delta\theta \quad (15)$$

Inserting (13) to (15), the transfer function of current synchronization system can be written in small-signal form as

$$\begin{bmatrix} \Delta i_d^c \\ \Delta i_q^c \end{bmatrix} = \begin{bmatrix} \Delta i_d \\ \Delta i_q \end{bmatrix} - \underbrace{\begin{bmatrix} 0 & -j_{q0} H_{iSOGI} G_{PLL} \\ 0 & j_{d0} H_{iSOGI} G_{PLL} \end{bmatrix}}_{G_{iPLL}(s)} \begin{bmatrix} \Delta u_d \\ \Delta u_q \end{bmatrix} \quad (16)$$



where  $i_{d0} = P_0 / u_{d0}$  and  $i_{q0} = Q_0 / u_{d0}$  are steady-state active and reactive currents in the network side.  $H_{isogi}$  is the proportional gain of SOGI in the current synchronization system.

### B. Current Controller Model

Fig. A2 depicts the current controller. Applying the Kirchhoff law in Fig. 3, the dynamic equations in small-signal form of the 4QC  $dq$  frame can be given by

$$\begin{bmatrix} \Delta v_d^c \\ \Delta v_q^c \end{bmatrix} = \begin{bmatrix} \Delta u_d^c \\ \Delta u_q^c \end{bmatrix} - \begin{bmatrix} R_m + sL_m & -w_0 L_m \\ w_0 L_m & R_m + sL_m \end{bmatrix} \begin{bmatrix} \Delta i_d^c \\ \Delta i_q^c \end{bmatrix} \quad (17)$$

The decoupling is added to the control loop in the form of an inner positive-feedback loop with a gain  $jw_0 L_m$ . From Fig. A2, the voltage reference  $\mathbf{U}^{ref}$  can be derived as

$$\begin{bmatrix} \Delta u_d^{ref} \\ \Delta u_q^{ref} \end{bmatrix} = \begin{bmatrix} \Delta u_d^c \\ \Delta u_q^c \end{bmatrix} - \begin{bmatrix} Kp_{CC} + \frac{Ki_{CC}}{s} & 0 \\ 0 & F_{CC} \end{bmatrix} \begin{bmatrix} \Delta i_d^{ref} - \Delta i_d^c \\ \Delta i_q^{ref} - \Delta i_q^c \end{bmatrix} - \begin{bmatrix} 0 & -w_0 L_m \\ w_0 L_m & 0 \end{bmatrix} \begin{bmatrix} \Delta i_d^c \\ \Delta i_q^c \end{bmatrix} \quad (18)$$

The input voltage  $v$  of the 4QC is generated by PWM modulation, considering the impact of the delay time  $T_d$  [25], we have

$$\begin{bmatrix} \Delta v_d^c \\ \Delta v_q^c \end{bmatrix} = \begin{bmatrix} G_d & 0 \\ 0 & G_d \end{bmatrix} \begin{bmatrix} \Delta u_d^{ref} \\ \Delta u_q^{ref} \end{bmatrix}, \quad G_d = \frac{1}{T_d s + 1} \quad (19)$$

where the time delay function  $G_d$  is a first-order function due to the single-updated PWM and we have  $T_d = 1.5 / f_s$ ,  $f_s$  is the switching frequency of the 4QC.

Equations (17)~(19) can be combined into (20).

$$\begin{cases} \begin{bmatrix} \Delta i_d^c \\ \Delta i_q^c \end{bmatrix} = \mathbf{H}_{CC}^{-1} \begin{bmatrix} 1-G_d & 0 \\ 0 & 1-G_d \end{bmatrix} \begin{bmatrix} \Delta u_d^c \\ \Delta u_q^c \end{bmatrix} + \mathbf{H}_{CC}^{-1} \begin{bmatrix} G_d F_{CC} & 0 \\ 0 & G_d F_{CC} \end{bmatrix} \begin{bmatrix} \Delta i_d^{ref} \\ \Delta i_q^{ref} \end{bmatrix} \\ \mathbf{H}_{CC}(s) = \begin{bmatrix} G_d F_{CC} + R_m + sL_m & (G_d - 1)w_0 L_m \\ (1-G_d)w_0 L_m & G_d F_{CC} + R_m + sL_m \end{bmatrix} \end{cases} \quad (20)$$

where  $\mathbf{Y}_{CC}(s)$  and  $\mathbf{G}_{CC}(s)$  are the inner (seen from the voltage  $u$ ) closed loop input admittance and transfer function.

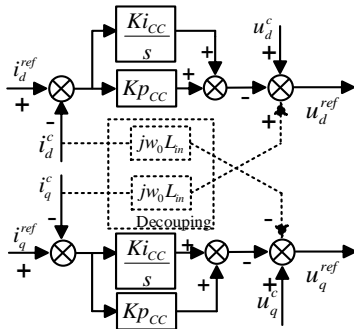


Fig. A2. Structure of the current controller (CC).

### C. Voltage Controller Model

Fig. A3 depicts the structure of voltage controller. The output of the VC is the active current reference  $i_d^{ref}$  for the CC and  $i_q^{ref}$  is set to zero for the unity power factor. In order to eliminate the dependence on the dc-link steady-state operating point  $v_{dc0}$ , it is an effective method to use the ‘energy’ error  $[(v_{dc}^{ref})^2 - v_{dc}^2] / 2$  instead of dc voltage error  $v_{dc}^{ref} - v_{dc}$  [21].

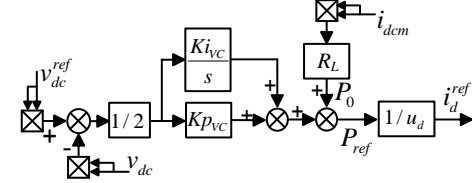


Fig. A3. Structure of the voltage controller (VC).

The dc-link voltage reference  $v_{dc}^{ref}$  is equal to steady-state voltage  $v_{dc0}$  in steady state. From Fig. A3, the active power reference and its linearized expression can be calculated as

$$P_{ref} = \left( Kp_{VC} + \frac{Ki_{VC}}{s} \right) \frac{(v_{dc}^{ref})^2 - (v_{dc})^2}{2} + P_0 \quad (21)$$

$$\Rightarrow \Delta P_{ref} = -F_{VC} v_{dc0} \Delta v_{dc}$$

where the load power  $P_0 = v_{dc} i_{dcm} = i_{dcm}^2 R_L = v_{dc}^2 / R_L$ ,  $R_L$  is load resistance. The active current reference  $i_d^{ref}$  and its linearized expression  $\Delta i_d^{ref}$  can be given by

$$i_d^{ref} = \frac{P_{ref}}{u_d} \Rightarrow \Delta i_d^{ref} = -\frac{F_{VC} v_{dc0}}{u_{d0}} \Delta v_{dc} - \frac{P_0}{u_{d0}^2} \Delta u_d \quad (22)$$

Combining (20), the network current can be written in small-signal form as

$$\Delta i_d = \left[ \mathbf{Y}_{CC(1,1)} - \frac{P_0}{u_{d0}^2} G_{CC(1,1)} \right] \Delta u_d + \mathbf{Y}_{CC(1,2)} \Delta u_q - \frac{v_{dc0} \mathbf{G}_{CC(1,1)} F_{VC}}{u_{d0}} \Delta v_{dc} \quad (23)$$

An ideal lossless model of the 4QC is assumed for simplification of modeling. Therefore, active power balance constraint between ac and dc side can be given by

$$u_d i_d = \frac{1}{2} C_d \frac{dv_{dc}^2}{dt} + P_0 \quad (24)$$

where the energy stored in the dc capacitor is  $C_d v_{dc}^2 / 2$  [21]. Equation (24) can be expressed in small-signal form as

$$\Delta P = i_{d0} \Delta u_d + u_{d0} \Delta i_d = s C_d v_{dc0} \Delta v_{dc} \quad (25)$$

Combing (23) and (25), the dc-link voltage can be written in small-signal form as

$$\Delta v_{dc} = \frac{\left[ i_{d0} + u_{d0} \mathbf{Y}_{CC(1,1)} - \frac{P_0}{u_{d0}^2} \mathbf{G}_{CC(1,1)} \right] \Delta u_d + \left[ u_{d0} \mathbf{Y}_{CC(1,2)} + i_{q0} \right] \Delta u_q}{v_{dc0} \left[ s C_d + \mathbf{G}_{CC(1,1)} F_{VC} \right]} \quad (26)$$

Thus, insert (26) back into (22), and solve the reference



current matrix, it can be written in a small-signal form as

$$\begin{bmatrix} \Delta i_d^{ref} \\ \Delta i_q^{ref} \end{bmatrix} = \begin{bmatrix} -G_{VC}^d & G_{VC}^q \\ 0 & 0 \end{bmatrix} \begin{bmatrix} \Delta u_d \\ \Delta u_q \end{bmatrix} \quad (27)$$

$\mathbf{G}_{VC}(s)$

where

$$\begin{cases} G_{VC}^d = \frac{F_{VC} [i_{d0} + u_{d0} \mathbf{Y}_{CC(1,1)} - i_{d0} \mathbf{G}_{CC(1,1)}]}{u_{d0} [sC_d + \mathbf{G}_{CC(1,1)} F_{VC}]} + \frac{P_0}{u_{d0}^2} \\ G_{VC}^q = \frac{F_{VC} [i_{q0} + u_{d0} \mathbf{Y}_{CC(1,2)}]}{u_{d0} [sC_d + \mathbf{G}_{CC(1,1)} F_{VC}]} \end{cases} \quad (28)$$

## APPENDIX B

TABLE BI

Symbol	Description	Simulation Value	Experimental Value
$e_s$	Network voltage	25 kV	240 V
$u_{d0}$	$d$ channel steady state voltage	1770 V	21 V
$u_{q0}$	$q$ channel steady state voltage	0 V	0 V
$k$	Ratio	15.54	11.43
$f_0$	Grid fundamental frequency	50 Hz	50 Hz
$L_{01}^*, L_{12}^*, L_{23}^*$	Network inductance in the secondary side	3.8 mH, 3.8 mH, 3.8 mH	4.2 mH, 4.2 mH, 0 mH
$R_{01}^*, R_{12}^*, R_{23}^*$	Network resistance in the secondary side	0.2 $\Omega$ , 0.2 $\Omega$ , 0.2 $\Omega$	0.2 $\Omega$ , 0.2 $\Omega$ , 0 $\Omega$
$L_m$	Leakage inductance of the transformer	6 mH	14 mH
$R_m$	Leakage resistance of the transformer	0.2 $\Omega$	0.1 $\Omega$
$C_d$	DC-link capacitance	10 mF	4.4 mF
$R_L$	Load resistance	20 $\Omega$	20 $\Omega$
$v_{dc0}$	DC-link voltage	3600 V	42 V
$P_0$	Active power	648 kW	88.2 W
$Q_0$	Reactive power	0 kW	0 kW
$f_s$	Switching frequency	1250 Hz	1250 Hz
$K_{sSOGI}$	SOGI-voltage gain	0.8	0.8
$K_{sSOGI}$	SOGI-current gain	1	1
$K_{pVC}$	VC-Proportional gain	0.4	0.1
$K_{iVC}$	VC-Integral gain	12	0.025
$K_{pPLL}$	PLL-Proportional gain	6	1
$K_{iPLL}$	PLL-Integral gain	100	30
$K_{pCC}$	CC-Proportional gain	3	6.5
$K_{iCC}$	CC-Integral gain	0.5	0.15

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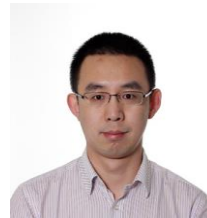
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